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TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Appli	cation Number	tion Number 09/632,425				
		Filing	Date	August 4, 2000	August 4, 2000			
		First I	Named Inventor	Geiger, Fabrice	Geiger, Fabrice			
		Group	Art Unit	2829				
		Exami	iner Name	Lisa A. Kilday				
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	ENCL	OSURES	(check all that apply)					
Fee Transmittal Form	Assignment Papers (for an Application)			After Allowance Communication to Group				
Fee Attached	Drawing(s)			Appeal Communication to Board of Appeals and Interferences				
Amendment / Response	Licensi	ing-relate	ed Papers	Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)				
After Final	Petition Routing Slip (PTO/SB/69) and Accompanying Petition			Proprietary Information				
Affidavits/declaration(s)	Petition to Convert to a Provisional Application			Status Letter				
Extension of Time Request	Power of Attorney, Revocation Change of Correspondence Address			Other Enclosure(s) (please identify below):				
Express Abandonment Request	Termin	nal Disclai	imer	Return Postcard				
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Certified Copy of Priority Document(s)	Rema	ırks	The Commissioner is a Deposit Account 20-14	authorized to charge any a 430.		fees	to	
Response to Missing Parts/ Incomplete Application			1		HNOL	I	RE	
Response to Missing Parts under 37 CFR 1.52 or 1.53					LL.	HAY 22	RECEIV	
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5-6-02

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TOWNSEND and TOWNSEND and CREWILLP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Examiner:

Lisa A. Kilday

Fabrice Geiger et al.

Art Unit:

2829

Application No.: 09/632,425

AMENDMENT

Filed: August 4, 2000

For: PROCESS FOR DEPOSITING A

POROUS, LOW DIELECTRIC

CONSTANT SILICON OXIDE FILM

Assistant Commissioner for Patents

Washington, D.C. 20231

In response to the Office Action mailed February 6, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Sir:

Please amend claim 10 as follows. The remaining claims are unamended, but are reproduced below for the Examiner's convenience and reference.

1. A method for forming an insulation layer over a substrate, the method comprising:

forming a surface sensitive silicon oxide layer over the substrate; and

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